CERTIFICATE OF TEAPPlicant(s): BRUCE B. De	CANSMISSION BY FAC	SIMILE (37 CFR 1.8)	Docket No. F15920030256US1
Application No. 10/605,727	Filing Date 10/22/2003	Examiner K. PICARDAT	Group Art Unit 2822
nvention: METHOD FOR EMICONDUCTOR DEVIC		ENCILISOLATION CONSUMP	RECEIVED CENTRAL FAX CENT
			MAY 1 3 2005
·		Transmittal and Amendment  (Identify type of correspondence)  and Trademark Office (Fax. No	767 873 0206
05/13/2005 (1)a(e)		and Hademark Office (Fax. No	). 703-872 <b>-</b> 9306
		(Typed or Printed Name of Person  LSALCU Signature)	Signing Certificate)
	Note: Each paper must ha	ve its own certificate of mailing.	
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AMENDMENT TRANSMITTAL LETTER (Large Entity)  Applicant(s): BRUCE B. DORIS ET AL.				Docket No. FIS920030256US1		
Application No.	Filing Dale 10/22/2003	Examiner K. Picardat	Customer N 29371	0.	Group Art Unit 2822	Confirmation No. 2726
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	AFTER AMENDMENT	PREV. PAID FOR	CLAIMS PRESENT		RATE	FEE
TOTAL CLAIMS	22 -	23 =	0	×	\$50.00	\$0.00
INDEP. CLAIMS	3 -	3 =	0	x	\$200.00	\$0.00
	nt Claims (check if appl	icable)	,			\$0.00
Diplease of A check in A check in Community of Any Daymont WARNINg included  Sear F. Sullivan	ange Deposit Account la the amount of ctor is hereby authorized cation or credit any over additional filing fees recepted to a patent application process by credit card. Form Piers on this form. Provide	to cover the fill to charge payment rpayment to Deposit quired under 37 C.F.I essing fees under 37 TO-2038.  form may become	Account 09-0458 R. 1.16. CFR 1.17.  public. Credit card intion and authorization.  Dated: May 13	nfo on	rmation shoul on PTO-2038.	d not be
Reg. No. 38,328 Cantor Colburn 55 Griffin Road Bloomfield, C'r 860-286-2929	South		the United States class mail in an envented P.O. Box 1450, Ale	Post relop xand	al Service with Sie addressed to "Cria, VA 22313-145"	
cc:			Typed or Prin	ted f	Name of Person Ma	iling Correspondence
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## RECEIVED CENTRAL FAX CENTER MAY 1 9 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	BRUCE B. DORIS, ET AL.	) Group Art Unit: 2822
Scriat No.:	10/605,727	)
Piled:	October 22, 2003	) Examiner: Picardat, K.
For:	METHOD FOR REDUCING SHALLOW TRENCH ISOLATION CONSUMPTION IN SEMICONDUCTOR DEVICES	) ) Confirmation No.: 2726 )

## **AMENDMENT**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In response to the Office Action mailed March 23, 2005, please amend the Application as follows: